

Chaitra A S
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Overview

Experience: 9 Years

Education

Ph. D. (Perusing) in Electrical Engineering from VTU University, Bangalore

M.Tech in VLSI and Embedded Systems from UTL technologies, Bangalore

B.E. in Electrical and Electronics Engineering from UBDT College of Engineering, Davangere

Research Interests

Power Systems

Publications

Research Publications:

National Journals / International Journals:

1) A paper titled "FPGA Implementation Of Lsb Steganography Core For Realtime Processing" has been published in the International Journal of Advanced Computer Technology, Issue 02, Vol.01, Page no.24-30.

National Conferences / International Conferences

1) A paper titled "Steganography by using improved least significant bit algorithm" was presented in the International Conference on at KSIT, Bangalore on August 13, 2014.

2) A paper titled “Depth control in Driver Pressing using Logic Controller” has been published in the National conference NCGEE-15 held at SKIT, Bangalore on 17th April 2014.

3) A Paper titled “FPGA Hardware implementation of LSB steganography for realtime processing” presented at National Level Conference on Emerging trends in computer science & technology ETCST-2013 at SKIT, Bangalore, 26th Oct 2013.

4) A Paper “Comparitive performance analysis shunt active filter and hybrid active active power filter” presented at National conference on Advanced and recent trends in Electrical and communication Engineering at SKIT, Bangalore on 28th April 2017.

Awards/Achievements